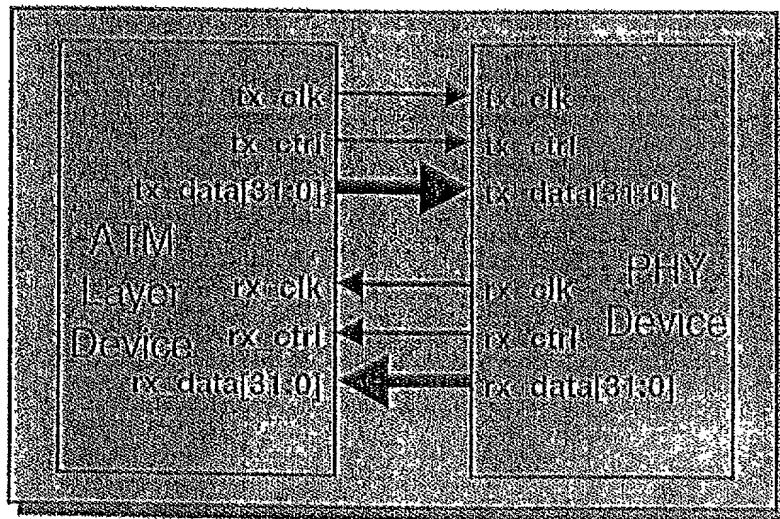


Fig. 1



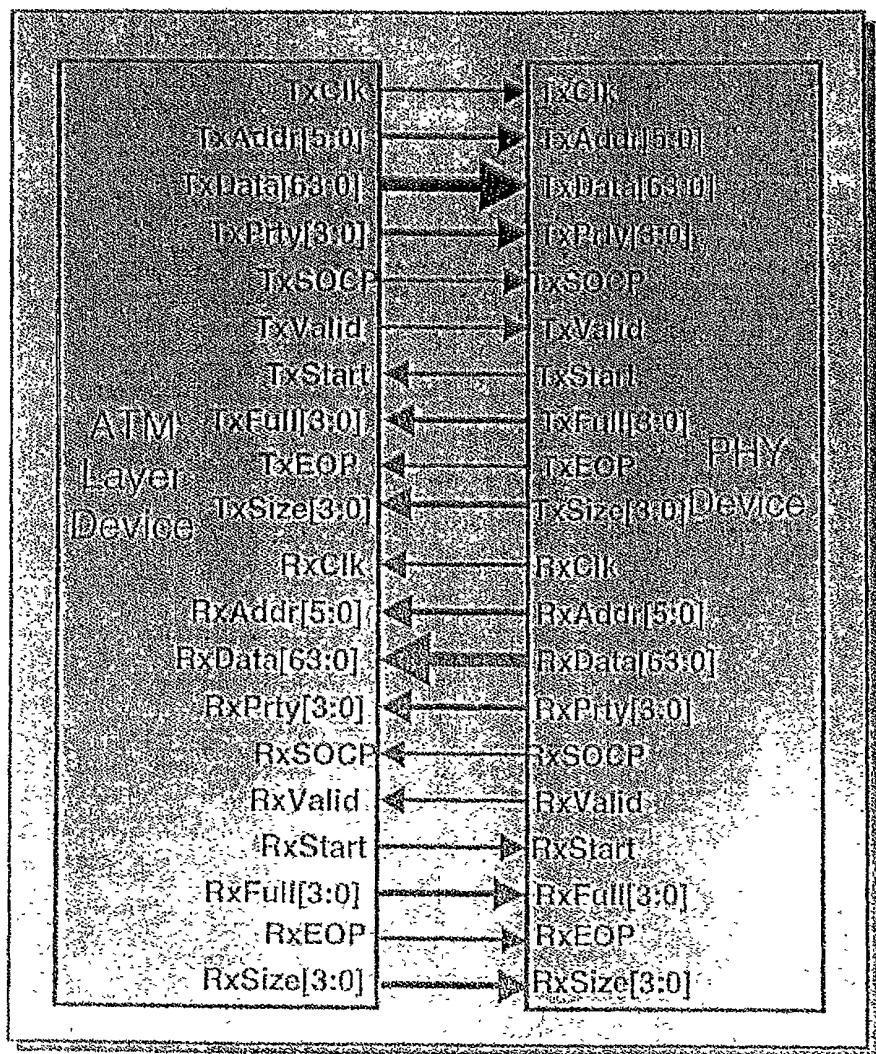
PRIOR ART

Fig. 2

Control Format						
31:30	29	28	27:26	25:24	23:8	7:0
00	SOC	28	EOP	25:24 reserved	aut[7:0]	
			27:26 >	23:16 cong[7:0]		
01	00		MOD	15:8 refl[7:0]		
		blk[3:0]		full[23:0]		
		0111		reserved		
10	0000			reserved	prty[7:0]	
		0001-1111		reserved		
11	0000			reserved	H[3]	
		0001-1111		reserved		

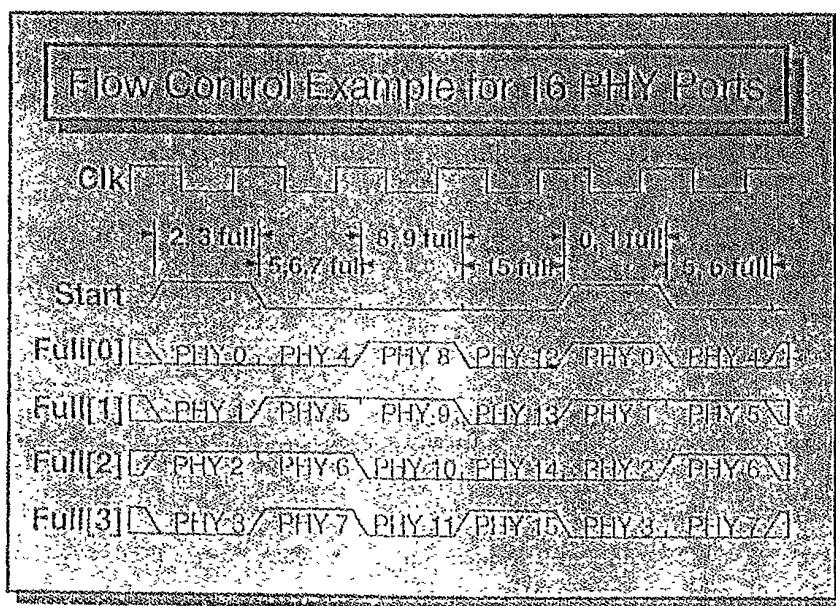
PRIOR ART

Fig. 3



PRIOR ART

Fig. 4.



PRIOR ART

Fig. 5

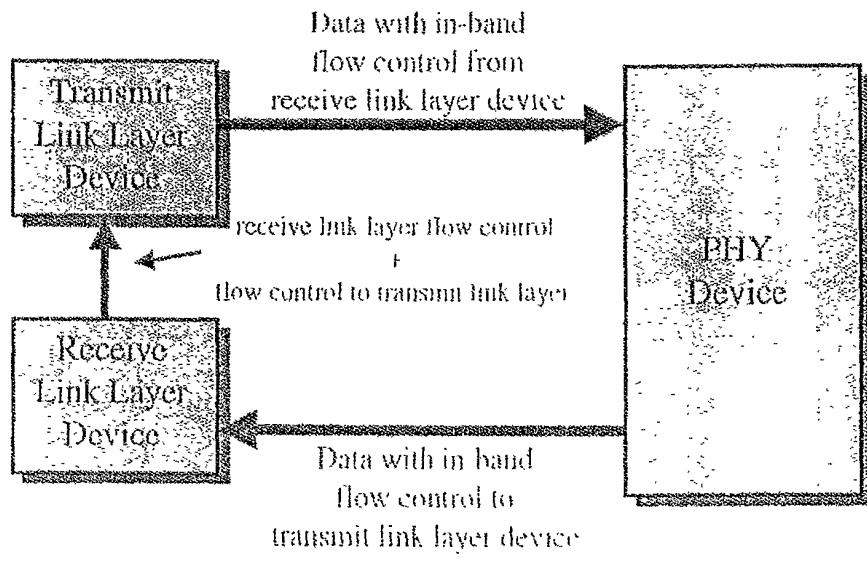


Fig. 6

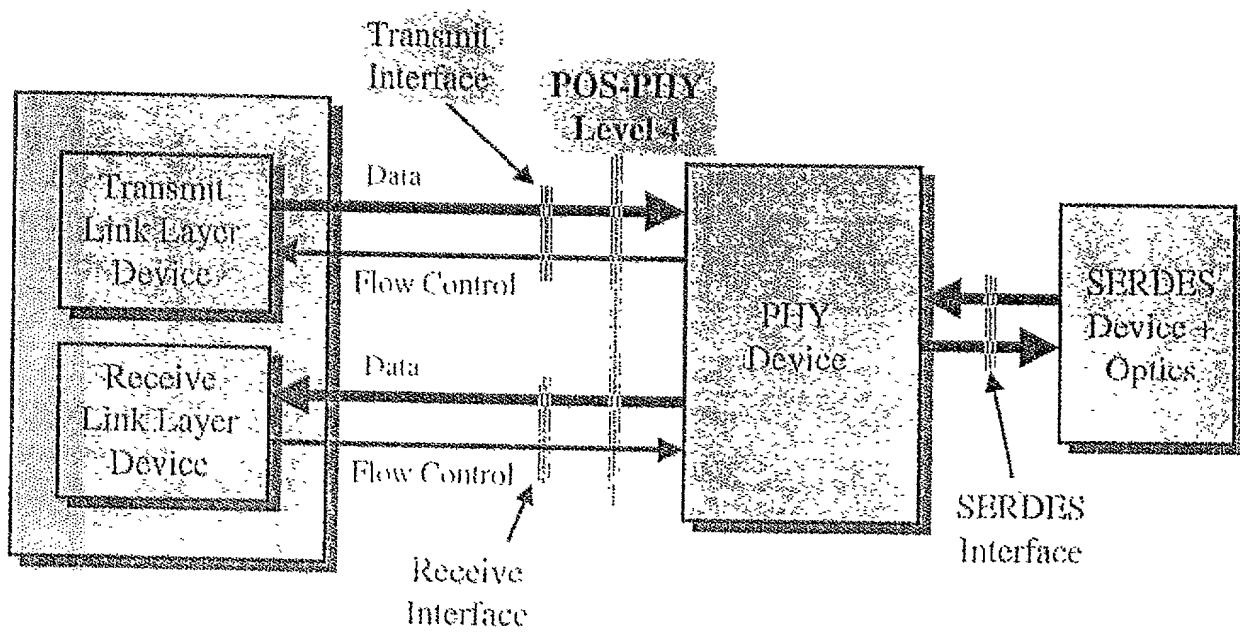


FIG. 7

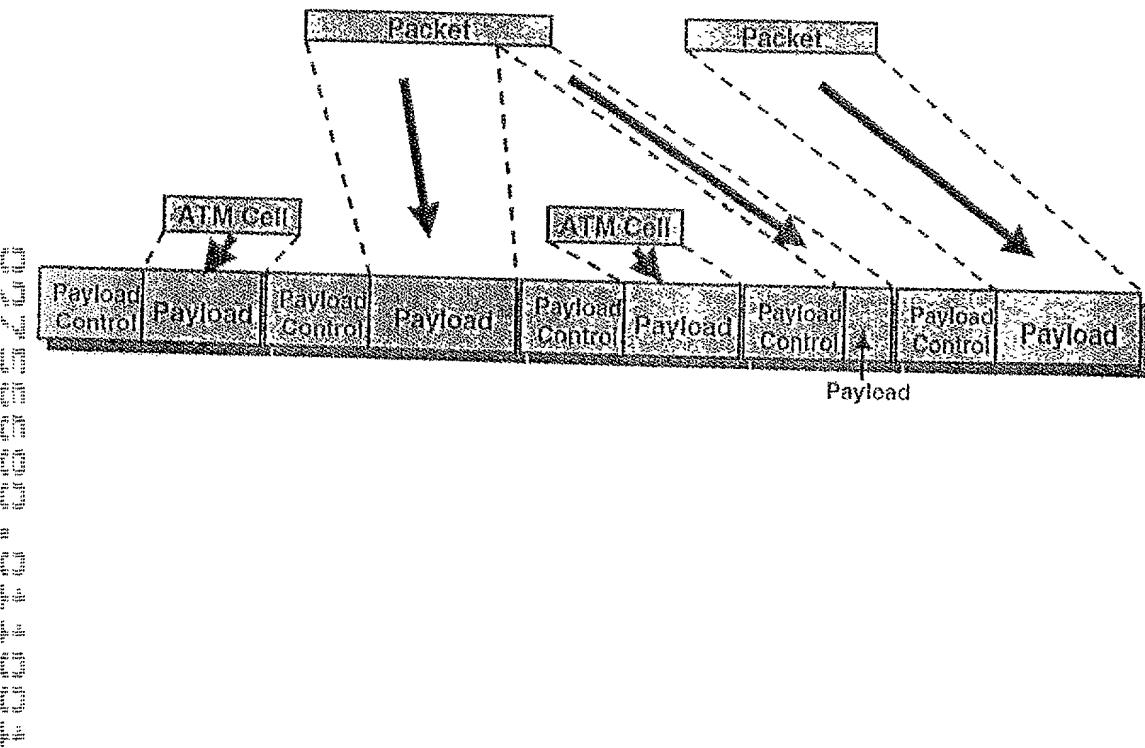


FIG 8

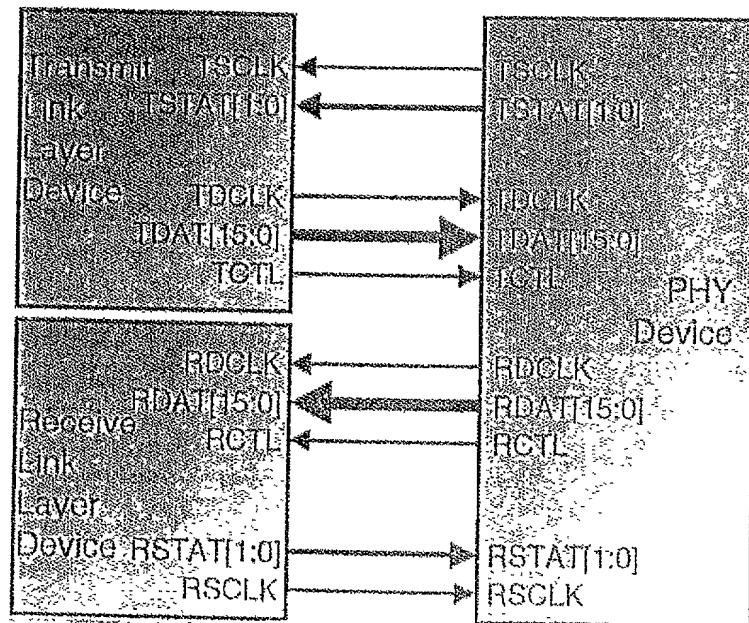


FIG. 9

	Bit 15	Bit 8	Bit 7	Bit 0
Data Word 1		Byte 1		Byte 2
Data Word 2		Byte 3		Byte 4
Data Word 3		Byte 5		Byte 6
Data Word 4		Byte 7		Byte 8
Data Word 21		Byte 41		Byte 42
Data Word 22		Byte 43		XX

FIG. 10

	Bit 15	Bit 8	Bit 7	Bit 0
Data Word 1	H1		H2	
Data Word 2	H3		H4	
Data Word 3	P1		P2	
Data Word 4	P3		P4	
Data Word 25		P45		P46
Data Word 26		P47		P48

FIG. 11

	Bit 15	Bit 8	Bit 7	Bit 0
Data Word 1	H1		H2	
Data Word 2	H3		H4	
Data Word 3	HEC		UDF	
Data Word 4	P1		P2	
Data Word 5	P3		P4	
Data Word 26		P45		P46
Data Word 27		P47		P48

FIG. 12

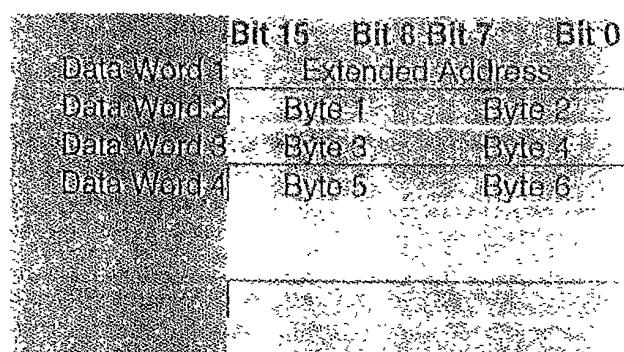


FIG. 13

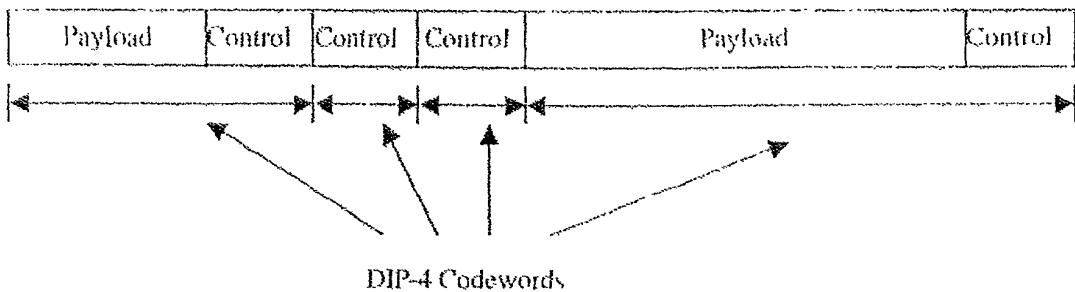


FIG. 14

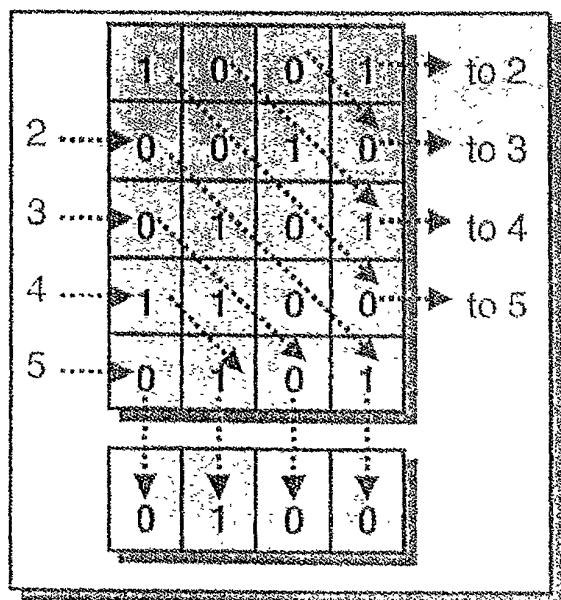


FIG. 15

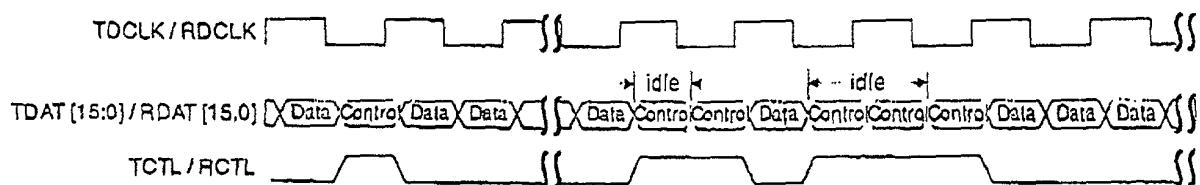


FIG. 16

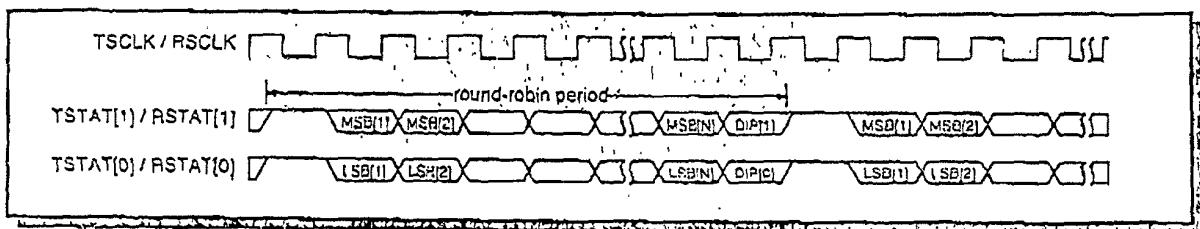


FIG. 17

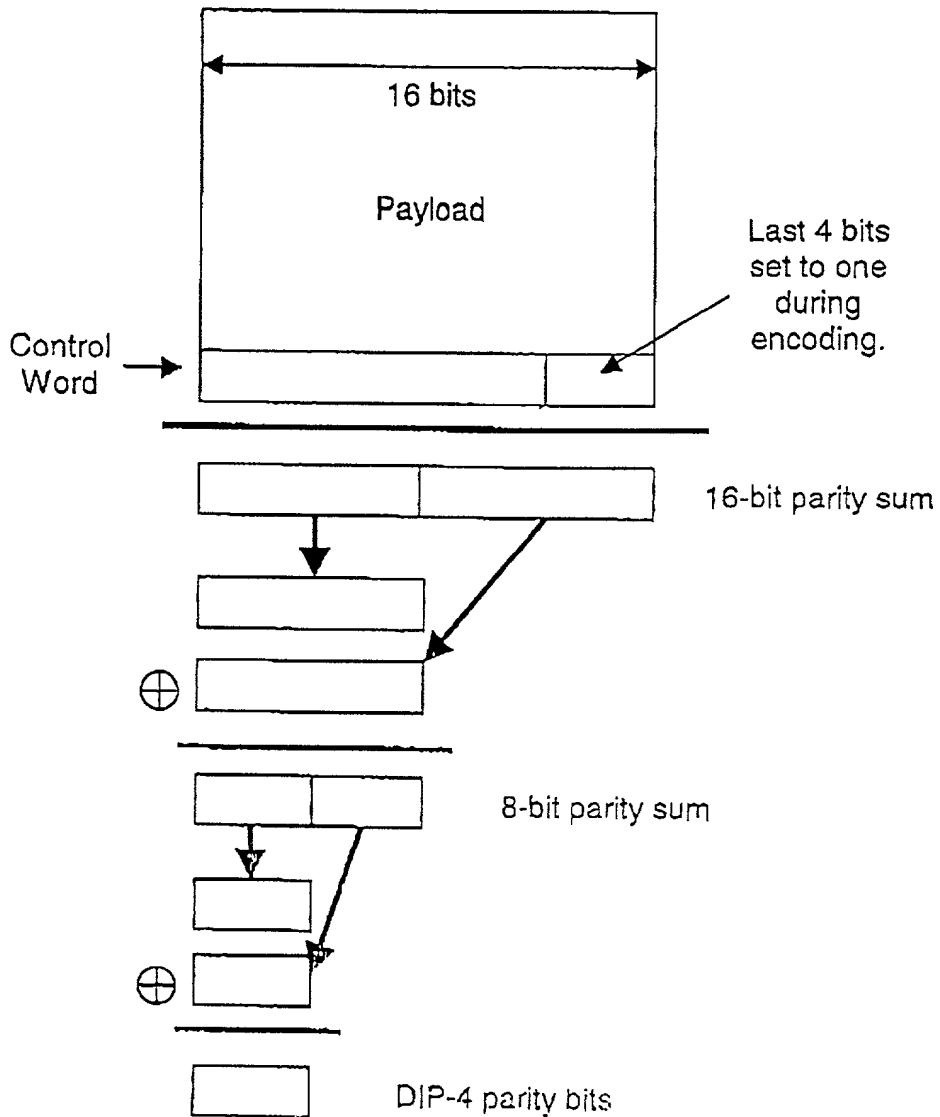


FIG. 18

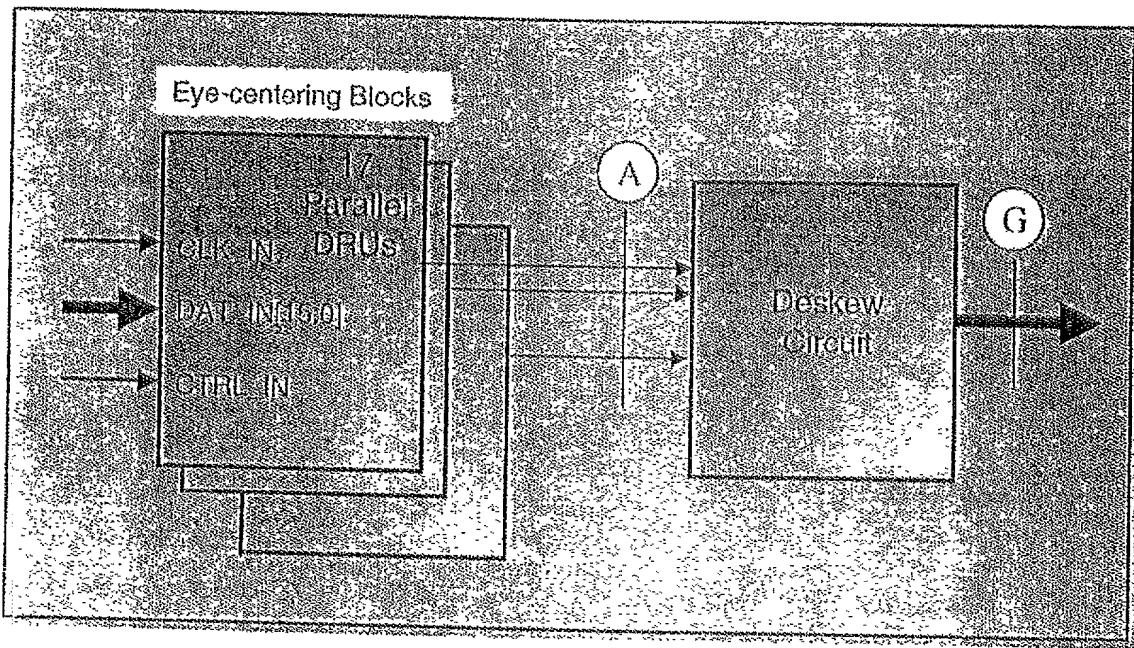


FIG. 19

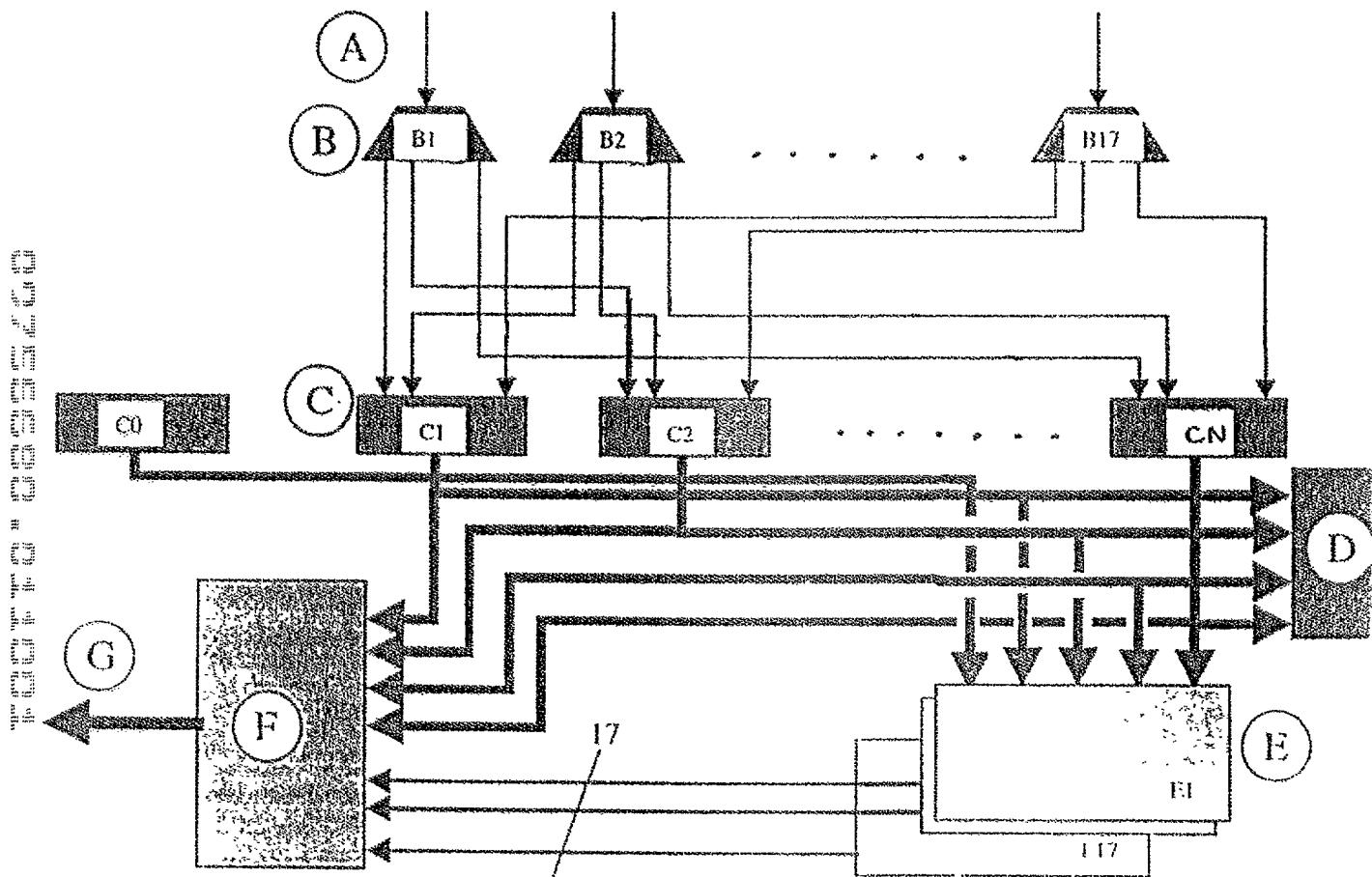


FIG. 20

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

